

LSI LOGIC

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LSI Logic Corporation
Legal Department - IP
1621 Barber Lane, MS D-106
Milpitas, CA 95035Date June 15, 2006Number of pages including cover sheet 2

To: US PTO

Mail Stop Status Request

TC 2829

Fax No. **571-273-8300**

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

Telephone (408) 433-7191
No.

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REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please commentApplication Number: 10/603,905
Filing date: June 25, 2003
First named inventor: Rafael Kedem
Attorney docket number: 03-0177

Transmitted herewith for filing via facsimile:

- Status Request Letter

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

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Date

Connie del Castillo

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of : Rafael Kedem

Serial No.: 10/603,905

Group Art Unit: 2829

Filed: 6/25/2003

For: Method and Apparatus For
Exposing Pre-Diffused IP Blocks In
A Semiconductor Device For
Prototyping Based On Hardware
Emulation

Atty Docket: 03-0177

STATUS REPORT

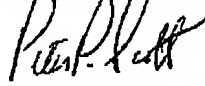
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

LSI Logic Corporation
1621 Barber Lane, MS D-106
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408-433-7475Date: 6/15/06

Respectfully submitted,



Peter Scott, Ph: [+1] 408-433-7191

Reg. No. 33,279